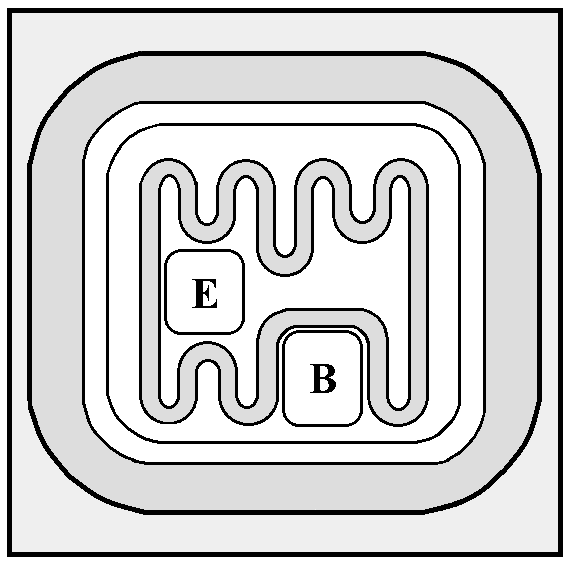
Chip back potential is the level which bulk silicon is maintained by on-chip connection, or it is the level to which the chip back must be connected when specifically stated below. If no potential is given the chip back should be isolated.



**.018”**

**.016”**

**Top Material: Al**

**Backside Material: Au**

**E= .0033” X .0035”**

**B= .0033” X .004”**

**Backside Potential: Collector**

**Process: 1985**

**APPROVED BY: DK DIE SIZE .016” X .018” DATE: 1/26/22**

**MFG: FAIRCHILD THICKNESS .008” P/N: 2N2219A**

**DG 10.1.2**

#### Rev B, 7/19/02